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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/393,951 03/24/2003 PAT 6,750,513 *(J w)*
which is a DIV of 10/101,162 03/20/2002 ABN

** FOREIGN APPLICATIONS *****

JAPAN 2001-318969 10/17/2001 *(J w)*

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** 05/21/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	DRAWING 17	5	5
Verified and Acknowledged <i>S. Noll</i> Examiner's Signature	Initials				

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TITLE *50I*

Semiconductor device and manufacturing method therefor *(J w)*

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